

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): **Wu, et al.**

Docket No.: **APRI-P016**

Serial No.: (Not yet assigned)

Group Art Unit: (Not yet assigned)

Filed: **04/07/2004**

Examiner: (Not yet assigned)

Title: **Method and Apparatus for Selective, Incremental, Reconfigurable and Reusable Semiconductor Manufacturing Resolution-Enhancements**

INFORMATION DISCLOSURE STATEMENT

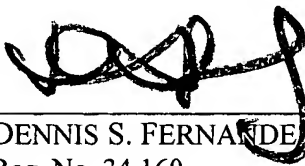
Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In keeping with the duty of candor and good faith owed to the Patent and Trademark Office, Applicants wish to bring the enclosed printed publications and U.S. patent references to the attention of the Examiner.

Applicant respectfully submits that Applicants' claimed invention is patentable over such references. Forms PTO/SB/08A and PTO/SB/08B listing these references are attached. It is understood the Office may find additionally relevant material in these or other references

Respectfully submitted,



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04/07/04

Date

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